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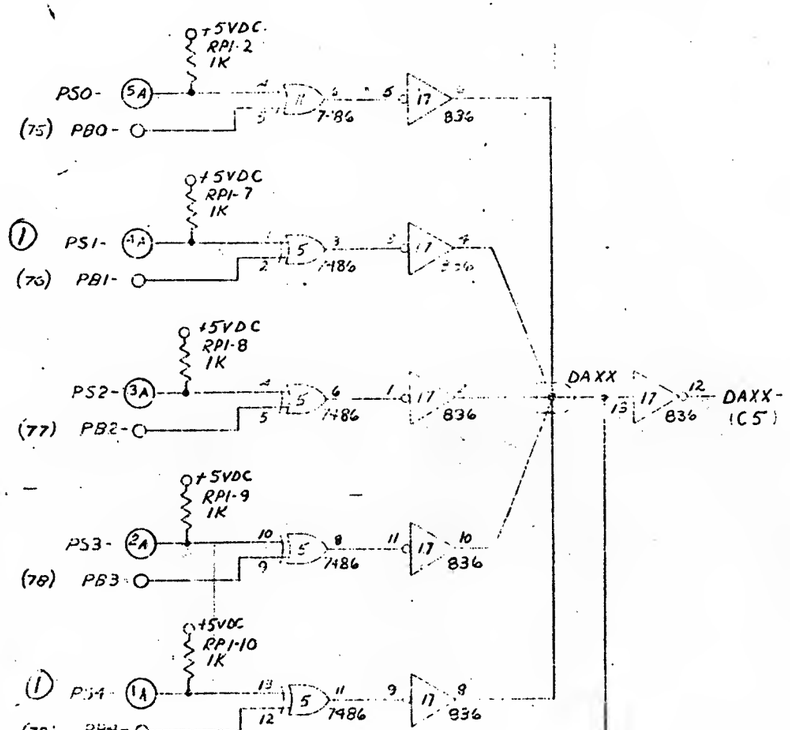
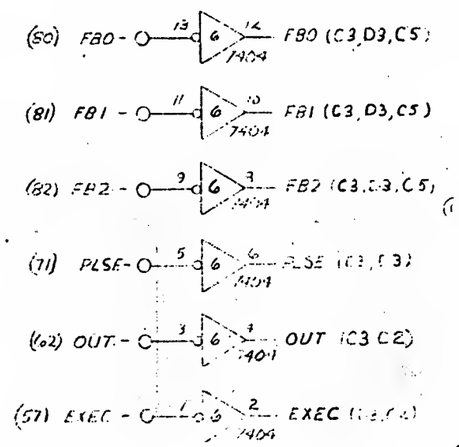
REVISIONS			
SYM	DESCRIPTION	DATE	APPROVED
	[illegible]		
21	[illegible]		
22	[illegible]		
23	[illegible]		

DEVICE ADDRESS DECODER

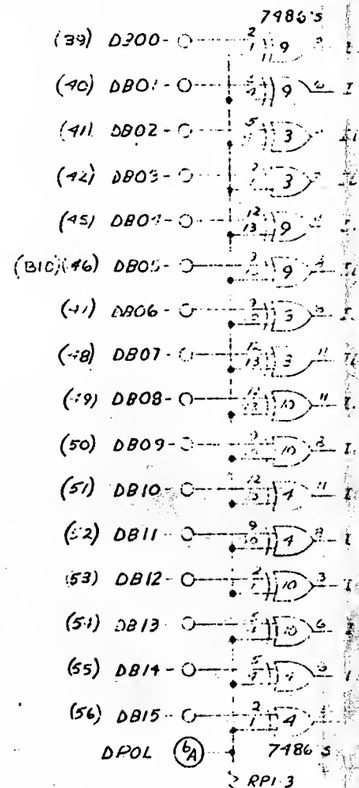


CONTROL BUS RECEIVERS



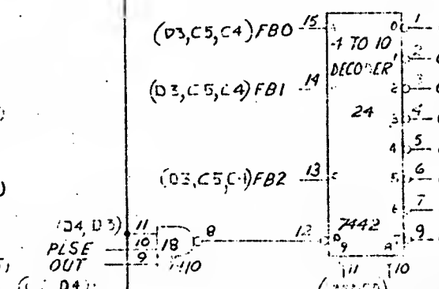
DATA BUS RECEIVER

7486's

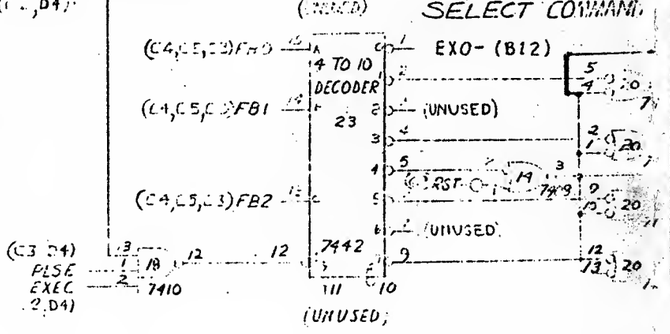


IDO0	(A2, A16 A15 A14)
IDO1	(A2, A16, A15, A14)
IDO2	(A2 A16 A15 A14
IDO3	(A2, A16, A15, A14)
IDO4	(B2, B16, B15, B14)
IDO5	(B2, B16, B15, B14)
IDO6	(B2 B16 B15 B14)
IDO7	(B2 B16, B15, B14)
IDO8	(A2)
IDO9	(A2)
IDO10	(A2)
IDO11	(B2)
IDO12	(B2)
IDO13	(B2)
IDO14	(B2)
IDO15	(B2)

OUTPUT COMMAND DECODER

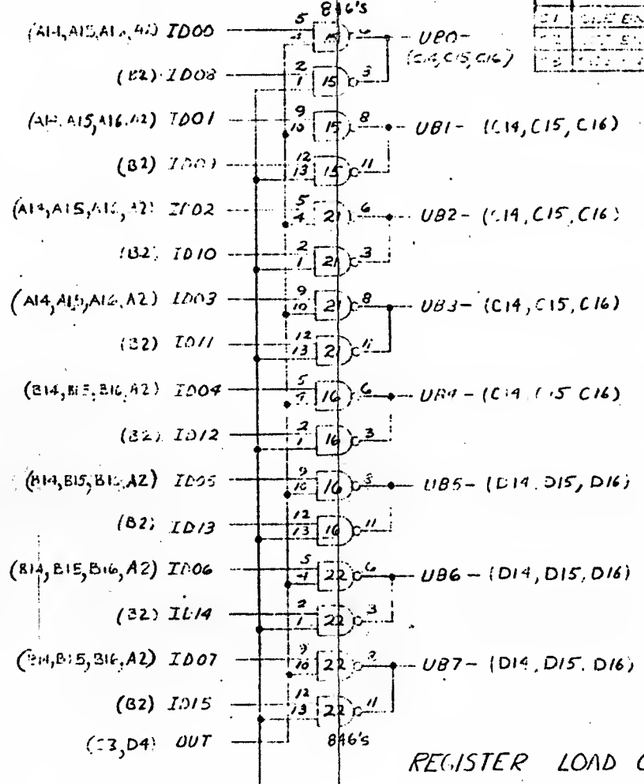


SELECT COMMAND DECODER



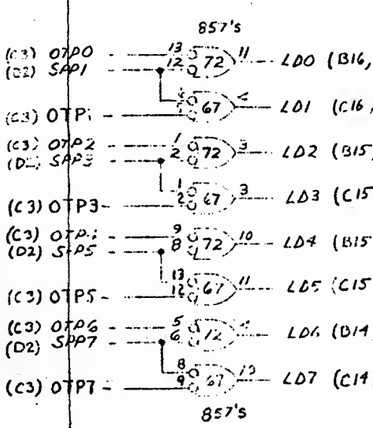
UPPER BYTE SELECTOR

8686's



REGISTER LOAD CONTROL

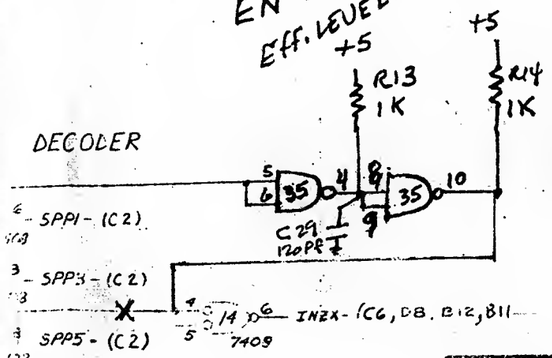
857's



(C3) (D4) EXEC +5VDC — 7486 — INEX- — 35 — +5V — R11 1K

EN 4580
Eff. LEVEL = C6

DECODER



MAR 29 1976

QTY	PART NO.	DESCRIPTION	MFR., VENDOR

COMPUTER AUTOMATION INC.

NEXT ASSY

LOGIC-
64 BIT OUTPUT

SIZE D — 52216-00

DATA REQUEST SENSOR

REVISIONS			
STM	DESCRIPTION	DATE	APPROVAL

ISB0 (A1) — 7404 — 7408 — 7486 — 9602 (200 NS, 620 pF, 2.00K, +5 VDC 1%) — PLUP (D5) — 846 — 7416 — REQ0 (C5), REQ0 – (A12), RDY0 (15/A)

(C1) LD0

ISB1 (A2) — 7404 — 7408 — 7486 — 9602 (200 NS, 620 pF, 2.49K, +5 VDC 1%) — PLUP (D5) — 846 — 7416 — REQ1 (C5), REQ1 – (A12), RDY1 (16/A)

(C1) LD1

ISB2 (A3) — 7404 — 7408 — 7486 — 9602 (200 NS, 620 pF, 2.49K, +5 VDC 1%) — PLUP (D5) — 846 — 7416 — REQ2 (C5), REQ2 – (A12), RDY2 (7/A)

(C1) LD2

ISB3 (A4) — 7404 — 7408 — 7486 — 9602 (200 NS, 620 pF, 2.49K, +5 VDC 1%) — PLUP (D5) — 846 — 7416 — REQ3 (C5), REQ3 – (A12), RDY3 (1/B)

(C1) LD3

ISB4 (A5) — 7404 — 7408 — 7486 — 9602 (200 NS, 620 pF, 2.49K, +5 VDC 1%) — PLUP (D5) — 846 — 7416 — REQ4 (C5), REQ4 – (A12), RDY4 (12/B), RPOL – (C5)

(C1) LD4

(B11,B12,E3,D2) ENZX –

(D8) SPOL –

SPOL (5/A) — RP2-2 1K — +5 VDC — 7404 — RPOL – (C6)

RPOL — RP2-5 1K — +5 VDC — RP2-9 1K — 856 — RPOL – (C5)

ISB5 (A1) — 836 — 7404 — 7408 — 7486 — 9602 (200 NS, 620 pF, 2.00K, +5 VDC 1%) — PLUP (D5) — 846 — 7416 — REQ5 (C5), REQ5 – (A12), RDY5 (15/A)

(C1) LD5

ISB6 (A2) — 836 — 7404 — 7408 — 7486 — 9602 (200 NS, 620 pF, 2.49K, +5 VDC 1%) — PLUP (D5) — 846 — 7416 — REQ6 (C5), REQ6 – (A12), RDY6 (14/A)

(C1) LD6

ISB7 (A3) — 836 — 7404 — 7408 — 7486 — 9602 (200 NS, 620 pF, 2.49K, +5 VDC 1%) — PLUP (D5) — 846 — 7416 — REQ7 (C5), REQ7 – (A12), RDY7 (13/A), RPOL – (D7)

(C1) LD7

(B11,B12,E3,D2) ENZX –

(D8) SPOL –

(D5,C3,C4) FB2
(D4,C2,C4) FB1
(D3,C1,C4) FB0

(A7) REQ0
(B7) REQ1
(D7) REQ2
(E7) REQ3
(C7) REQ4
(A5) REQ5
(B5) REQ6
(D6) REQ7

9 (E) MUX — 74151 — 7A — 856 — SER – (C4)

DATA REQUEST SENSOR

SPARE GATES TABLE	
IC NO.	UNUSED OUTPUT PINS
11	8, 11
7B	(CLEAR) 13
12	3
14	8
30	6
31	3, 6, 8
25	6, 8
41	4, 8, 10

RP1-6 +5 VDC — 1K — PLUP (A7, B7, C7, D7, A6, B6)

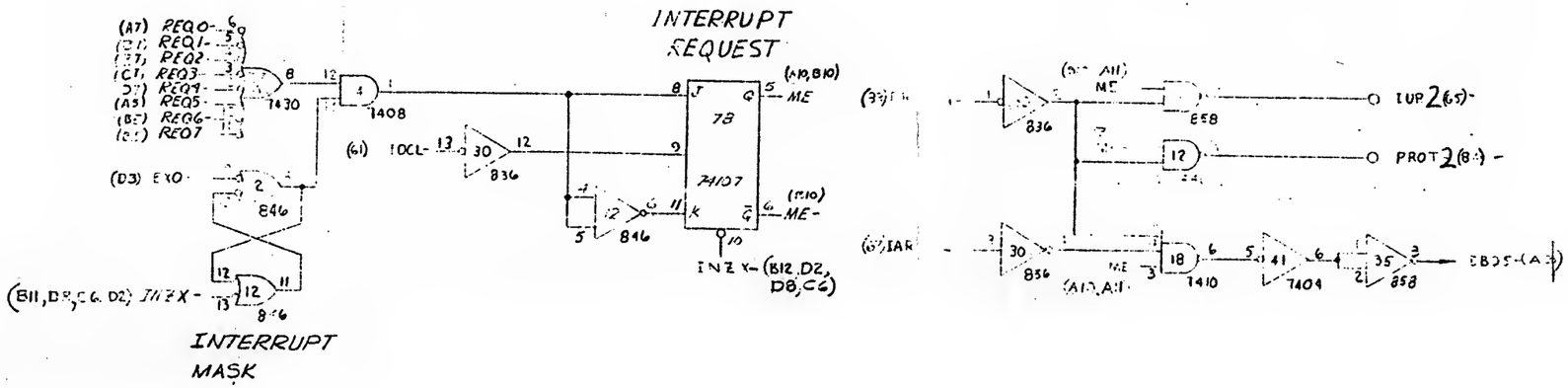
5 CAPACITORS 1.2 pF ARE 500V 5%
4. CAPACITORS .01 µF ARE 25 VOLTS 10%
CAPACITORS 2.2 µF ARE 20 VOLTS 10%
3. ALL RESISTORS ARE 5% 1/4 W.
2 FOR ASSEMBLY DRAWING SEE DO 52216-00
1. REF. DESIGNATIONS NOT USED R1, R2

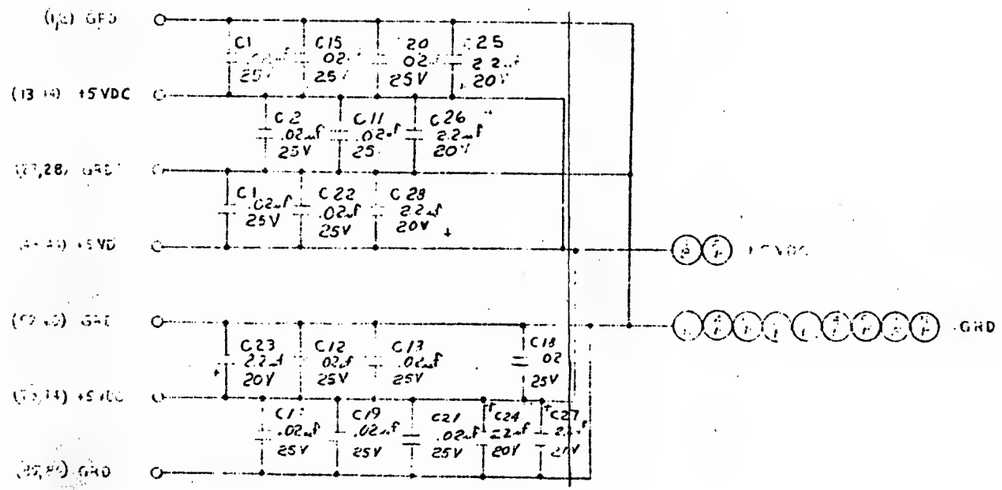
NOTES: UNLESS OTHERWISE SPECIFIED

SIZE	DWG NO	REV
D	52216-00	
SCALE		SHT 2 OF 4

REVISIONS			
SYM	DESCRIPTION	DATE	APPROVAL

INTERRUPT REQUEST LOGIC





SIZE	DWG NO	REV
D	20-52216-00	23
SCALE=	SHT. 3 OF 4	